

2/27/05

AMENDMENT TRANSMITTAL LETTER (Small Entity)

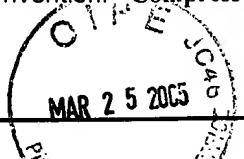
Applicant(s): Sewall et al.

Docket No.

880.0011.U1(US)

Application No. 09/977,713	Filing Date October 12, 2001	Examiner Coby, F.	Customer No. 29683	Group Art Unit 2171	Confirmation No. 3294
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Invention: Compressed Data Structure and Decompression System

**COMMISSIONER FOR PATENTS:**

Transmitted herewith is an amendment in the above-identified application.

Applicant claims small entity status. See 37 CFR 1.27

The fee has been calculated and is transmitted as shown below.

CLAIMS AS AMENDED

	CLAIMS REMAINING AFTER AMENDMENT	HIGHEST # PREV. PAID FOR	NUMBER EXTRA CLAIMS PRESENT	RATE	ADDITIONAL FEE
TOTAL CLAIMS	61 -	53 =	8	x \$25.00	\$200.00
INDEP. CLAIMS	5 -	5 =	0	x \$100.00	\$0.00
Multiple Dependent Claims (check if applicable) <input type="checkbox"/>					\$0.00
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT					\$200.00

No additional fee is required for amendment.

Please charge Deposit Account No. _____ in the amount of _____

A check in the amount of \$200.00 to cover the filing fee is enclosed.

The Director is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account No. 50-1924

- Any additional filing fees required under 37 C.F.R. 1.16.
- Any patent application processing fees under 37 CFR 1.17.

Payment by credit card. Form PTO-2038 is attached.

WARNING: Information on this form may become public. Credit card information should not be included on this form. Provide credit card information and authorization on PTO-2038.

Dated: March 23, 2005

I certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to "Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450" [37 CFR 1.8(a)] on

3/23/05

(Date)

Signature of Person Mailing Correspondence

Ann Okrentowich

Typed or Printed Name of Person Mailing Correspondence

CC:



IN THE U.S. PATENT AND TRADEMARK OFFICE

Appl. No. : 09/977,713
Applicant : Patrick M. Sewall et al.
Filed : 10/12/2001
TC/AU : 2171
Examiner : Coby, Frantz

Docket No. : 880.0011.U1(US)
Customer No. : 29683

Title : COMPRESSED DATA STRUCTURE AND DECOMPRESSION SYSTEM

Mail Stop Non-Fee Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT

Sir:

In response to the non-final Office Action dated December 23rd, 2004, please amend the above referenced application as follows:

Amendments to the Specification begins on page 2 of this paper.

Amendments to the Drawings: None.

Amendments to the Claims begins on page 3 of this paper.

Remarks/Arguments begins on page 15 of this paper.

Appendix None.

This Amendment is filed within the shortened statutory period for reply recited in the referenced Office Action, and no petition for extension of time or fee is deemed necessary or due. Should the undersigned representative be mistaken, please consider this as a petition for an extension of time necessary to effect his Amendment and charge Deposit Account No. 50-1924 for any required fee deficiency.

Appl. No. 09/977,713
Amdt. Dated March 23, 2005
Reply to Office Action of December 23, 2004

AMENDMENTS TO THE WRITTEN DESCRIPTION:

In the written description, please delete paragraphs [0024] and [0047]. There is no Fig. 16 in this application.

Please also amend paragraph [0048] as follows:

[0048] The tables of Figs. 41—16-11-15 are examples only. The selected operations and sizes of library 50 and look-up strings 64 were designed for an ARM processor. In this case, library 50 contains approximately 16,000 code strings 54 each containing thirty-two bits. With a library of this size, as CPU 28 executes decompression program 38, the most accessed code-strings 54 in library 50, if not the entire library 50, naturally migrate to processor cache 46. The same is true for the most repeated instructions of decompression program 38. Consequently, CPU 28 is able to inflate compressed data structure 36 accessing volatile and non-volatile memory as few times as possible. For portable computing systems utilizing an ARM processor, this feature increases the speed at which a program or other data is inflated resulting in better performance and an increased battery life.